

	Class	Subclass
ISSUE CLASSIFICATION		


U.S. UTILITY Patent Application

**O.J.P.E.**

PATENT DATE

APPLICATION NO. 09/762981	CONF/PRIOR D F	CLASS 712	SUBCLASS 34	ART UNIT 2183	EXAMINER Chang, Ed Petersen
Andrea Oisiati Dominic Paul McCarthy Computer architecture containing processor and coprocessor					

[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b> Sheets Draw.      Figs. Draw.      Print Fig.			<b>CLAIMS ALLOWED</b> Total Claims      Print Claim for O.G.	
	<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. _____ (Assistant Examiner)      (Date)			<b>NOTICE OF ALLOWANCE MAILED</b> 	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____	<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____ _____ (Primary Examiner)      (Date)			<b>ISSUE FEE</b> Amount Due      Date Paid	
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